

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|----------------|-----------------|------------------|---------|------------------|
| L1 | 322 | (706/15).CCLS. | USPAT; USOCR | OR | OFF | 2007/09/04 10:26 |

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|-------|------|---|--|------------------|---------|------------------|
| L1 | 0 | digital neural network circuit board | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/04 07:13 |
| L2 | 1 | digital neural network circuit | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/04 07:14 |
| L3 | 207 | digital neural network | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/04 07:14 |
| L4 | 6 | neuron with processor with memory with array | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/04 08:12 |
| S1 | 26 | functional neural network | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/04 03:50 |
| S2 | 183 | functional near5 neural network | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/04 03:40 |
| S3 | 6 | neural network and (neuron with processor with memory with array) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/04 08:12 |

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|----|----|---|--|-----|----|------------------|
| S4 | 3 | neural network and (neuron with pointer with array) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/04 03:52 |
| S5 | 21 | neural network and (neuron near5 pointer) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/04 07:13 |

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| S1 | 4 | structural neuron | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2007/08/27 12:55 |
| S2 | 4 | elemental neuron | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2007/08/27 12:59 |
| S3 | 1 | S1 and S2 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2007/08/27 12:44 |
| S4 | 15 | neuron near3 pointer near3 neuron | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2007/08/27 17:03 |
| S5 | 4 | binary tree neural network | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2007/08/27 17:03 |

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[Scholarly articles for neural network neuron having processor and memory](#)



A **neural network** component for an intrusion detection system - Debar - Cited by 195

Apparatus including a pair of **neural networks having** ... - Baji - Cited by 26

Neural Networks and Analog Computation: Beyond the ... - Siegelmann - Cited by 259

Neural Network Processor

This can be done by **having** a separate counter for it and **memory** block for it. ... Now, with this, you could have a **neural network processor** that could have ...

neuralnetworks.ai-depot.com/NeuralNetworks/1426.html - 13k - [Cached](#) - [Similar pages](#)

Find patent Dynamic memory processor

Computerized voting information system **having** predefined content and voting ... The dynamic **memory processor** is part of a new **neural network** technology that ...

www.eipa-patents.org/Finance/Find-patent-Dynamic-memory-processor-500021.htm - 26k

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Learning method for neural network having discrete interconnection ...

Therefore, learning in a **neural network having** a discrete ... includes a computer 20

having a processor 22, memory 24 and Input/Output (I/O) controller 26. ...

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Dynamic memory processor - US Patent 6560582

A dynamic **memory processor** for time variant pattern recognition and an input data dimensionality reduction is provided **having** a multi-layer harmonic **neural** ...

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Learning method for neural network having discrete interconnection ...

for each interconnection of the optical **neural network having** a discrete includes a computer 20 **having a processor 22, memory 24** and Input/Output ...

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Feed forward neural network for unary associative memory - Patent ...

Feed forward **neural network** models for associative content addressable **memory** ... from only the one **processor neuron having** the greatest current input. ...

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[PDF] Arithmetic-unit and processor design for neural networks - Neural ...

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Consider a **processor-memory** pair in which the **processor** consists of ... for **neural-network** processing. (An alternative to **having** an SIMD or vector ...

ieeexplore.ieee.org/iel5/7132/19261/00890174.pdf - [Similar pages](#)

[PDF] Intelligent-memory architecture for artificial neural networks ...

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Lund Associative Array **Processor** (Lucas),. 9. which used a **memory** with a Response time of a **neural network** of four layers,. each **having n neurons**, ...

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Scholarly articles for +elemental +structural +neurons sigma-pi model



[Evolutionary Induction of Sparse Neural Trees](#) - Zhang - Cited by 48

[Artificial Neural Networks for Civil Engineers: ...](#) - Garrett - Cited by 18

[A Bayesian evolutionary approach to the design and ...](#) - Zhang - Cited by 3

Freeman Web Archives

Structural overview. As the **elemental** unit from which the nervous system is In some **models of neurons**, it is replaced by a set of compartments in ...

sulcus.berkeley.edu/FreemanWWW/manuscripts/ID6/92.html - 213k -

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KEYWORD INDEX abort-replan scheme, 619 acceleration, method of ...

... 651 electrophysiological data, 651 Keyword Index 1177 **elemental** motions, 27

shunting inhibition, 764 **sigma-pi** units, 35 sigmoid circuit, ...

books.nips.cc/papers/txt/nips04/1175.txt - 20k - [Cached](#) - [Similar pages](#)

[PDF] 1 Introduction

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weight numbers were reduced by connecting all hidden layer **neurons** to the differently resourced **sigma-pi** networks. This **elemental** heterogeneity means ...

adt.lib.swin.edu.au/uploads/approved/adt-VSWT20070423.121528/public/02chapters1-5.pdf

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[PS] The Neural Basis of Cognitive Development: A Constructivist Manifesto

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change during development in terms that correspond to **structural** changes in in wholesale changes in representation with each new **elemental** change; ...

[ftp://ftp.cnl.salk.edu/pub/steve/quartz.const.ps.Z](http://ftp.cnl.salk.edu/pub/steve/quartz.const.ps.Z) - [Similar pages](#)

EP526504 Sutherland european software patent - Artificial neural ...

The size and shape of these **neurons** vary widely, but the **structural** plan always The **elemental** operation within the representative linear search ...

gauss.ffii.org/PatentView/EP526504 - 229k - [Cached](#) - [Similar pages](#)

THE NEURAL BASIS OF COGNITIVE DEVELOPMENT:

Mainen, Z.F., Joerges, J., Huguenard, J.R. & Sejnowski, T.J. (1995) A **model** of spike initiation in neocortical pyramidal **neurons**. *Neuron* 15:1427-1439. ...

www.bbsonline.org/Preprints/OldArchive/bbs.quartz.html - 164k - [Cached](#) - [Similar pages](#)

CONSTRUCTIVIST MANIFESTO

A **model** of spike initiation in neocortical pyramidal **neurons**. ... **Sigma-Pi** learning: On radial basis functions and cortical associative learning. ...

www.geocities.com/ResearchTriangle/System/8870/books/brainconstruction.html - 194k - [Cached](#) - [Similar pages](#)

[PDF] THESE Emmanuel GUIGON MODELISATION DES PROPRIETES DU CORTEX ...

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Sigma-pi unit. Most neural **models** used formal **neurons**. Levy WB, Colbert CM,

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neuron with memory

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Recognition Memory

Visual recognition **memory** involves a reduction in neuronal activity. ... Given the number of synapses typically present on a **neuron** (1000 or more), ...

www.bris.ac.uk/synaptic/research/projects/memory/recmem.htm - 20k - [Cached](#) - [Similar pages](#)

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Natural Memory Supplement
Proprietary Herbal Alert! Formula
For Natural **Memory** & Brain Health
NaturallyDirect.net

How Are Memories Stored and Retrieved? -- Miller 309 (5731): 92 ...

How its various components interact during **memory** encoding and retrieval is ... called synapses, between **neurons**, whereas long-term **memory** (lasting days or ...

www.sciencemag.org/cgi/content/full/309/5731/92 - [Similar pages](#)

Neuron - Memory Alpha, the Star Trek Wiki

From **Memory Alpha**, the free Star Trek reference. Jump to: navigation, search. **Neurons** are cells of the nervous system. They receive chemical messages and in ...

memory-alpha.org/en/wiki/Neuron - 22k - [Cached](#) - [Similar pages](#)

Neuron Model Memory Efficiency

I've developed a new **neuron** model that will allow for the learning of all input sites to take up 10000 times less **memory**. This **neuron** model, by the way, ...

neuralnetworks.ai-depot.com/NeuralNetworks/1441.html - 8k - [Cached](#) - [Similar pages](#)

Neurons enter memory competition : Article : Nature Reviews ...

Han et al. show that **neurons** compete to take part in fear **memory** formation and that a **neuron's** level of the transcription factor cyclic AMP-response element ...

www.nature.com/nrn/journal/v8/n6/full/nrn2193.html - [Similar pages](#)

Young and excitable: new neurons in memory networks - Nature ...

In the adult hippocampus, a brain region that is important for **memory**, new **neurons** are generated continuously. A study now shows that these newly generated ...

www.nature.com/neuro/journal/v10/n3/full/nn0307-273.html - [Similar pages](#)

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Memory: Neurons enter memory competition - Cell Signaling Update ...

Neurons compete to take part in fear **memory** formation, and a **neuron's** level of the transcription factor cyclic AMP-response element binding protein (CREB) ...

www.signaling-gateway.org/update/updates/200706/nrn2155.html - [Similar pages](#)

Memory Neuron: Operating Characteristics for the Memory Component ...

In this paper an attempt is made to deduce the principal operating characteristics that a **neuron** would need in order to qualify as the basic **memory** ...

www.pnas.org/cgi/content/abstract/77/4/2305 - [Similar pages](#)

Neurons on the Map - The Fifth Neuron Satellite Meeting

FINAL PROGRAMME - **NEURONS AND MEMORY** (2004). October 21, 2004. 6:30-9:30

Opening reception and poster session. October 22, 2004. 7:30 Registration open ...

www.neuron-meeting.com/04program.htm - 9k - [Cached](#) - [Similar pages](#)